

## **RAM P. RUSTAGI, Ph.D.**

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### **Executive Summary**

- **30+** years of global experience in Academics and IT Industry
- Guided 3 Ph.D scholars.
- Experience spectrum: Academia, R&D Institutes, IT Startups.
- Award winner for ACM Compute'21 Teaching Challenge.
- Conducted multiple hands on workshops under IEEE Technical sponsorship
- Publications in Int'l Conf/journals, patents. TPC member for IEEE ANTS and ADCOM, Column writer at ACCS journal of Computing and Communications.
- **Research and Teaching Interests:** Computer Networks, Security, Machine Learning, Big Data, Algorithms, Open Source technologies, Application Performance Mgmt.
- Community Service: teaching of programming skills to non-CS background persons

### **Academics & Personal Particulars**

- Ph.D (Computer Science & Engg), Indian Institute of Technology, Delhi, India, 1998.
- M.Tech, Indian Institute of Science, Bangalore, India, 1981.

### **Chronology of Professional Experience:**

#### **10/2022-onwards: University of Maryland County, MD, USA.**

*Visiting Professor, CSEE Dept, and DoIT*

- Setting up CyberRange for UMBC to enable hands-on learning in Computer Security
- Teaching "Computer Security" to graduate students

#### **04/2018-onwards: KS Institute of Technology, Bangalore, India.**

*Professor AIML Dept, and Key Resource Person. On Sabbatical from Oct 2022.*

- Teaching undergraduate students with experiential learning and gamification approach
  - Algorithms, Networks, Big Data, Machine Learning, Security
- Consultancies, Invited speaker, industry interaction, etc.

#### **06/2017-03/2018: Cavisson Systems Inc, USA/Bangalore India**

*Mentor Technology, and Head - Bangalore development centre*

- Implementation of Alert efficacies using event correlation and anomaly detection
- Development of Security information and Event Management using ML analytics.

#### **11/2010 - 05/2017: PES University (formerly PESIT). Bangalore India.**

*Professor, Department of Computer Science and Engineering.*

- Taught B.E., M.Tech, MCA students, and engaged in course curriculum design
- Conducted Conference, Hands-on workshops, Faculty Development program etc.
- Consultancies, Invited speaker, industry interaction, etc.

#### **12/2014 - 07/2016: Smartron India Pvt Ltd, Bangalore India (On sabbatical from PES)**

*Principal Engineer, Head Cloud operations.*

- Cloud based application delivery for smartphone backup, synchronisation and upgrade

#### **01/2008 - 10/2010: Kirusa Software Pvt Ltd, Bangalore/Delhi India.**

*Vice President – Application & Services*

- Development of Telecom VAS applications, deployed in India and globally
- Managed geographical distributed teams at Delhi, Jaipur, Bangalore and NJ (USA).

**04/2002 – 01/2008: Pronto Networks Inc. (CA, USA; Bangalore India)**

*Vice President – Technology (CA, USA) & Head India Development Centre, Bangalore*

- Development of OSS/BSS suite of products catering to Wireless ISPs
- Application algorithms redesign to achieve order of performance improvement,.
- Development of public wifi hotspots products, roaming access e.g. airports, hotels, ...
- Managed and mentored 100+ professionals in India offices.

**10/2000 – 02/2002: Abeona Networks, Inc., Fremont, CA, USA.**

*Director - Engineering*

Provided technical leadership in the following areas:

- Unified Management with Single System Image - Cluster administration, monitoring
- Content management - staging, versioning, deployment, synchronization.
- N+1 fault tolerance including hot standby architecture and real time recovery
- Design of distributed Memory Cache and algorithmic performance optimization

**10/1998 – 10/2000: Savera Systems Inc, Murray Hill, NJ, USA.**

*Director - Engineering (1999-2000), Senior Member of Technical Staff (1998-99)*

- Development of Financial Statement, Reconciliation and Settlement modules. This product resulted in huge savings of time & money for dispute resolution
- Requirement analysis and specifications for Interconnect agreements for European countries including country specific National Interconnect agreements.
- Call Detail Record (CDR) format specification and Mediation device functionality.

**1991 – 1998: I.I.T. Delhi, India.**

*Research Associate (1995 – 1998). ERNet/CSE Dept (1991 – 1995)*

- *Ph.D. Research Work:* Proposed a framework for deadlock free concurrency control.
- Responsible for ERNet program research and operations at IIT Delhi.
- Consultancy in Asia Pacific region on Network Design, Application design, development and performance tuning, teaching and training etc

**1990 (Mar to Nov): Harris Corporation, DTS Division, Novato, CA, USA.**

*Consultant.*

- Enhanced routing scheme from a single PBX to multiple PBXs connected via ISDN Primary Rate Interface (PRI) trunks including CCITT Q.931 implementation.

**1981 – 1990: C-DoT & HCL Limited New Delhi, India.**

*Software R&D Engineer/Team Lead.*

- Developed Global Call Routing feature to route telephone calls in real time.
- Defined the messages architecture for inter process communication
- Development of COBOL compiler using 8085 Assembly level language

**Personal Details:**

**Citizenship:** Indian

**Language:** Proficient in Verbal, Written and spoken English, Hindi

**Other Details:**

Stanford University Code-In-Place Section Leader: Python programming:

<https://codeinplace.stanford.edu/cip3/home>

ACM Compute'21 award: [https://isigcse.acm.org/challenges/challenge\\_compute21](https://isigcse.acm.org/challenges/challenge_compute21)

Technical column writer: <https://journal.accsindia.org/>

Personal website: <https://www.rprustagi.com/>